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L22 and L4	53

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L23</u>	L22 and l4	53	<u>L23</u>
<u>L22</u>	L21 and l11	172	<u>L22</u>
<u>L21</u>	(compar\$5 near3 (parity bit\$1)) same (error\$1 near2 condition\$1)	300	<u>L21</u>
<u>L20</u>	(compar\$5 near3 (parity bit\$1)) same (error\$1 near2 condition41)	0	<u>L20</u>
<u>L19</u>	l3 and L18	3	<u>L19</u>
<u>L18</u>	l7 and L17	128	<u>L18</u>
<u>L17</u>	l14 and L6	720	<u>L17</u>
<u>L16</u>	l14 and L15	137	<u>L16</u>
<u>L15</u>	712/\$.ccls.	13976	<u>L15</u>
<u>L14</u>	((ahift register\$1) same (parity bit\$1)) and l4	1808	<u>L14</u>
<u>L13</u>	l5 and L11	19	<u>L13</u>
<u>L12</u>	l10 and L11	25	<u>L12</u>
<u>L11</u>	@ad<20000630	19425182	<u>L11</u>

<u>L10</u>	l3 and L9	47	<u>L10</u>
<u>L9</u>	(timer\$1) and l8	4008	<u>L9</u>
<u>L8</u>	(parity bit signal\$1) and l1	20431	<u>L8</u>
<u>L7</u>	(detect\$5 near3 (error\$1 or fault\$1)) near4 (state machine\$1)	9224	<u>L7</u>
<u>L6</u>	714/\$.ccls.	59077	<u>L6</u>
<u>L5</u>	l3 and L4	46	<u>L5</u>
<u>L4</u>	l1 and l2	5473	<u>L4</u>
<u>L3</u>	(output\$5 near2 signal\$1) near4 ((error\$1 or fault\$1) near2 condition\$1)	679	<u>L3</u>
<u>L2</u>	compar\$5 same (detect\$5 near3 (error\$1 or fault\$1))	41355	<u>L2</u>
<u>L1</u>	(detect\$5 near3 (error\$1 or fault\$1)) same (processor\$1 or microprocessor\$1)	23804	<u>L1</u>

END OF SEARCH HISTORY